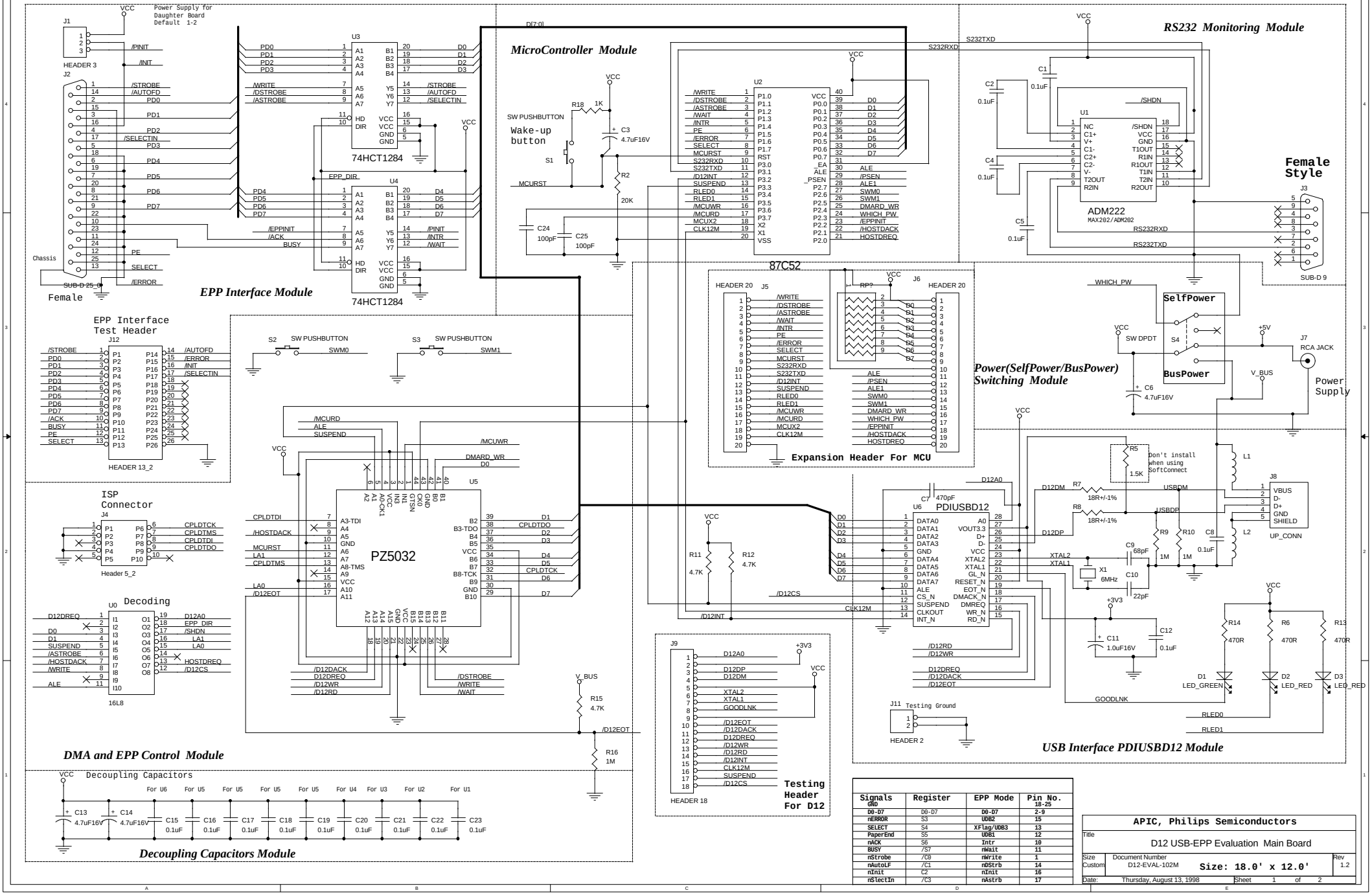

Signals	Register	EPP Mode	Pin No. 18-25
GND			
D0-D7	D0-D7	D0-D7	2-9
nERROR	S3	UDB2	15
SELECT	S4	XFlag/UDB3	13
PaperEnd	S5	UDB1	12
nACK	S6	Intr	10
BUSY	/S7	nWait	11
nStrobe	/C0	nWrite	1
nAutoLF	/C1	nDStrb	14
nInit	C2	nInit	16
nSlectIn	/C3	nAstrb	17

APIC, Philips Semiconductors	
Title	D12 USB-EPP Evaluation Main Board
Size: Custom	Document Number: D12-EVAL-102M
Size: 18.0' x 12.0'	Rev: 1.2
Date: Thursday, August 13, 1998	Sheet 1 of 2